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Fig.1
(PRIOR ART)

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Fig. 3a

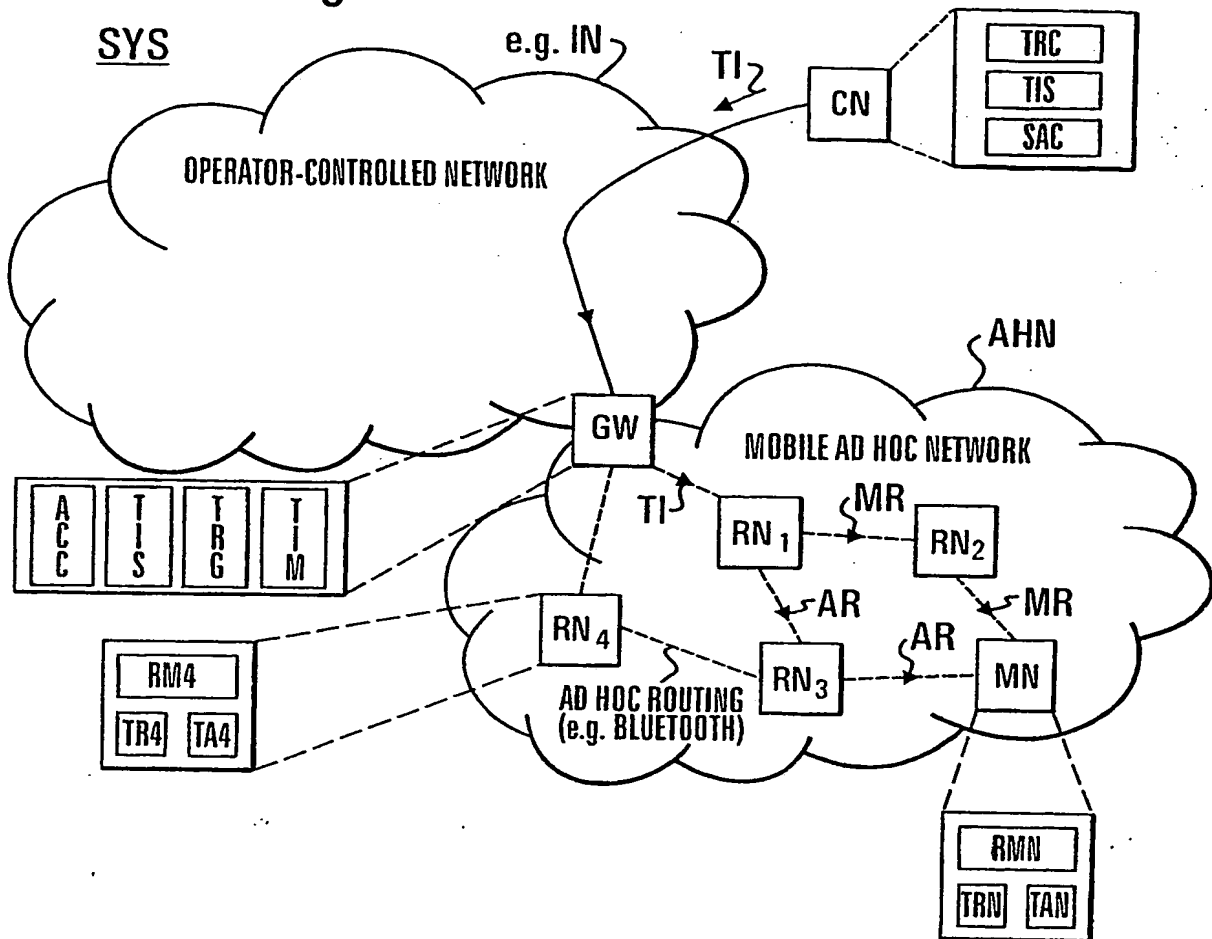
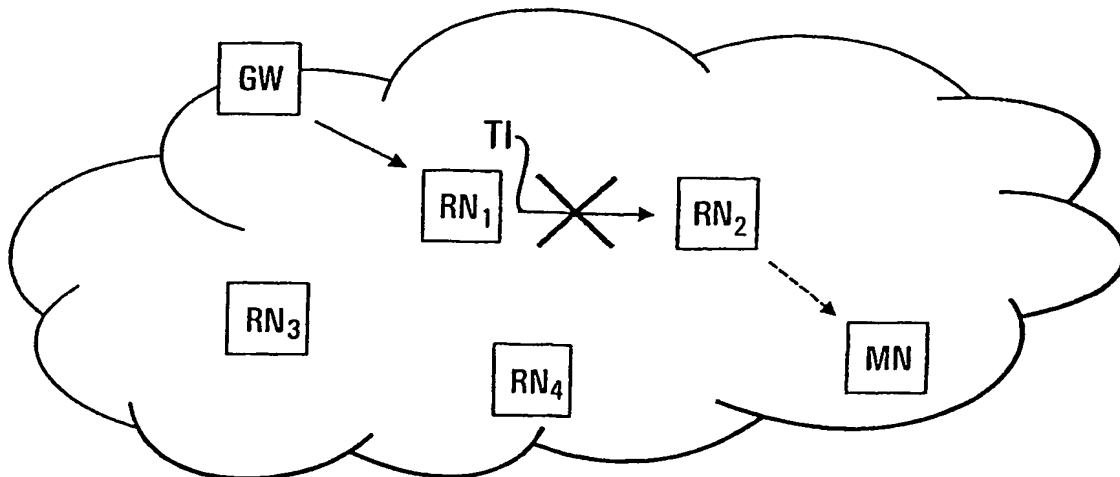


Fig. 3b



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Fig. 4a

TIM ACC

SA	DAM	TRT	TA	CH	TIS
SAC	2MB	64kbits/s	TA4	7ct	
SAC	1MB	128kbits/s	TAN	2ct	
.	
.	
.	

TCH

Fig. 4b

TIM ACC ACKM/ACKS4
ACKSN

SA	DAM	TRT	TA	CH	ACK	TIS'
SAC	2MB	64kbits/s	TA4	7ct	ACTA4	
SAC	1MB	128kbits/s	TAN	2ct	ACTAN	
.	
.	
.	

TCH

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Fig. 4c

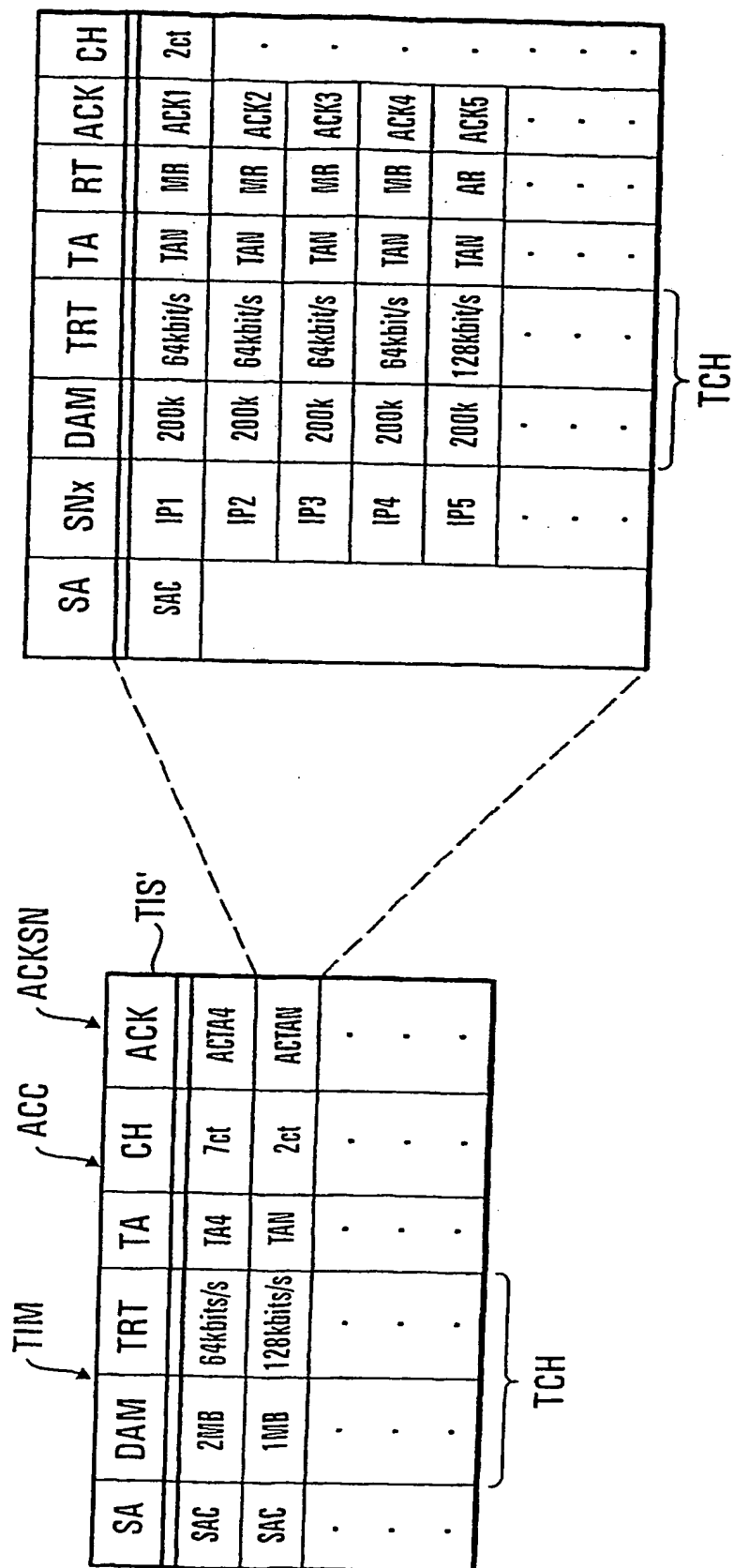


Fig. 5a

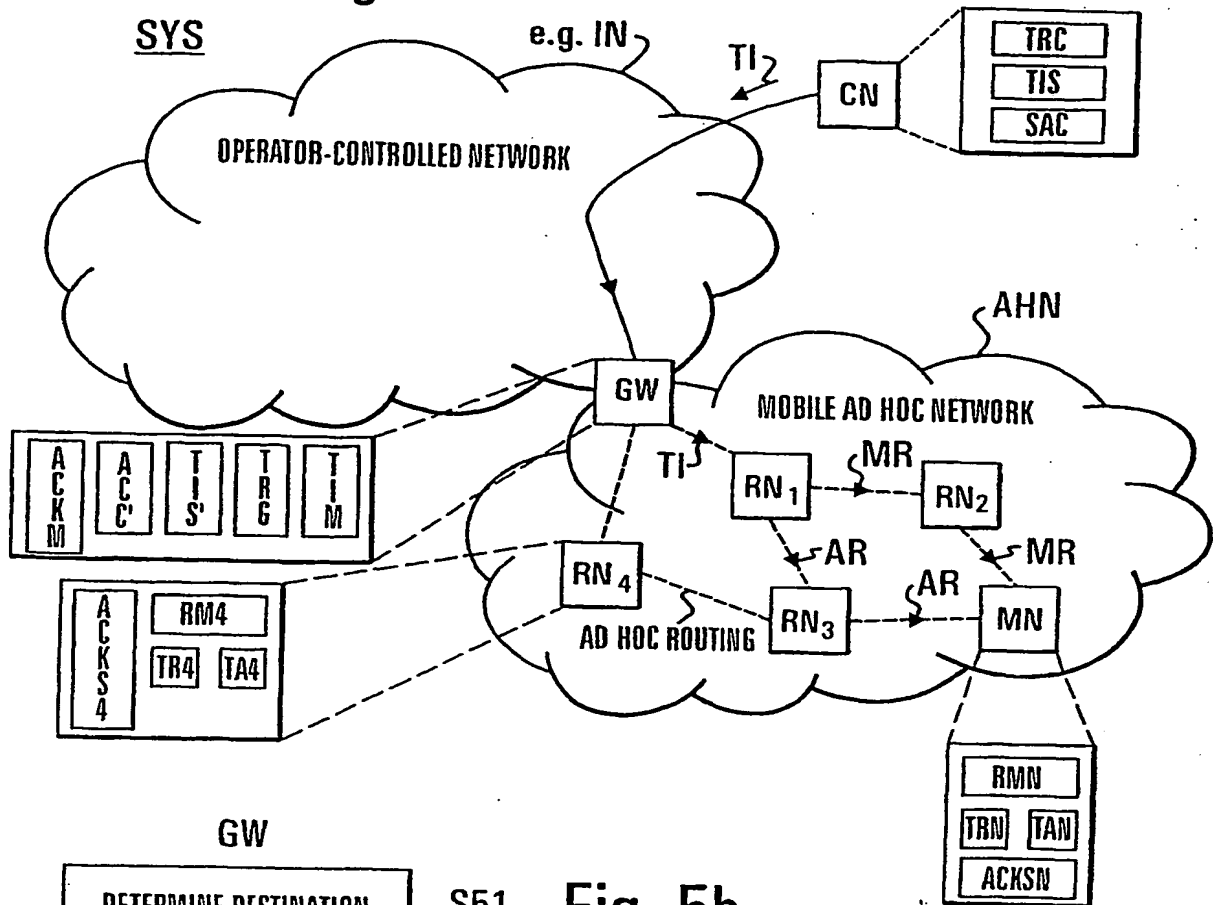
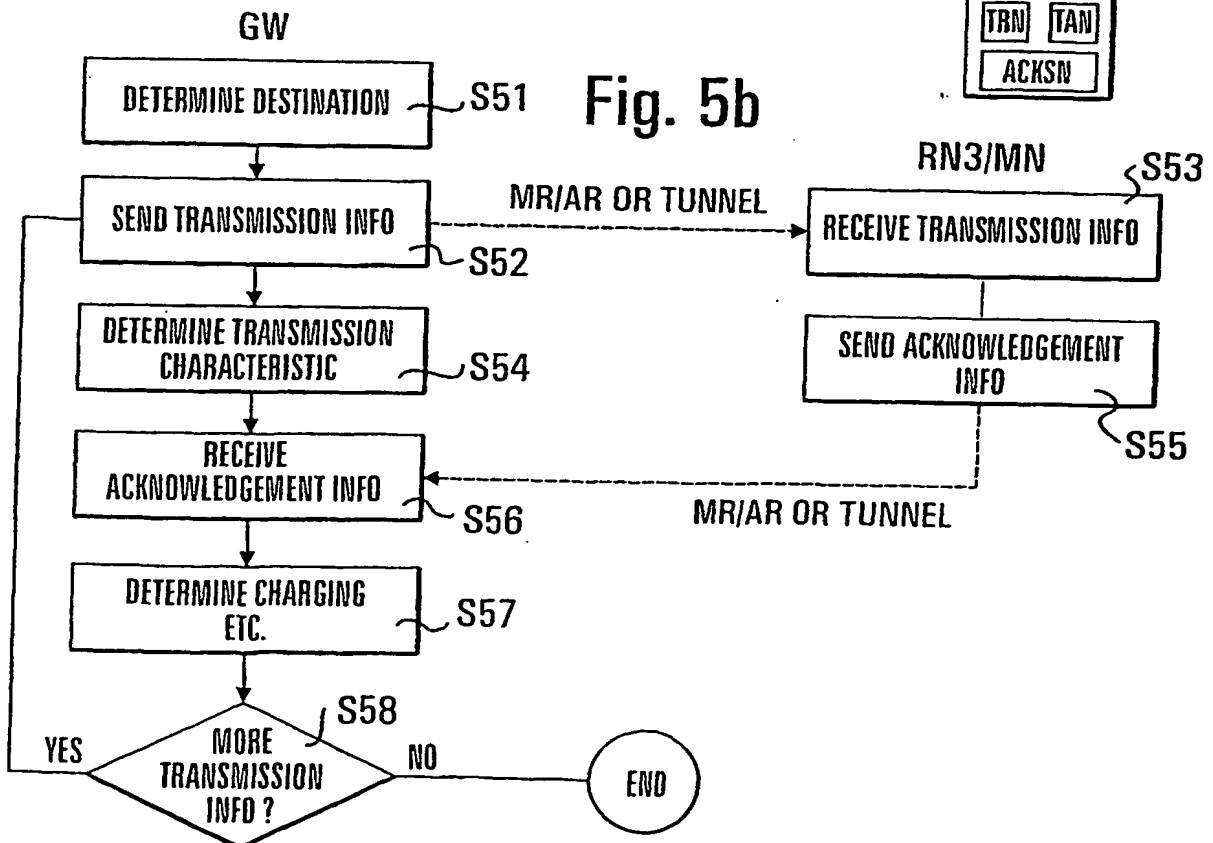
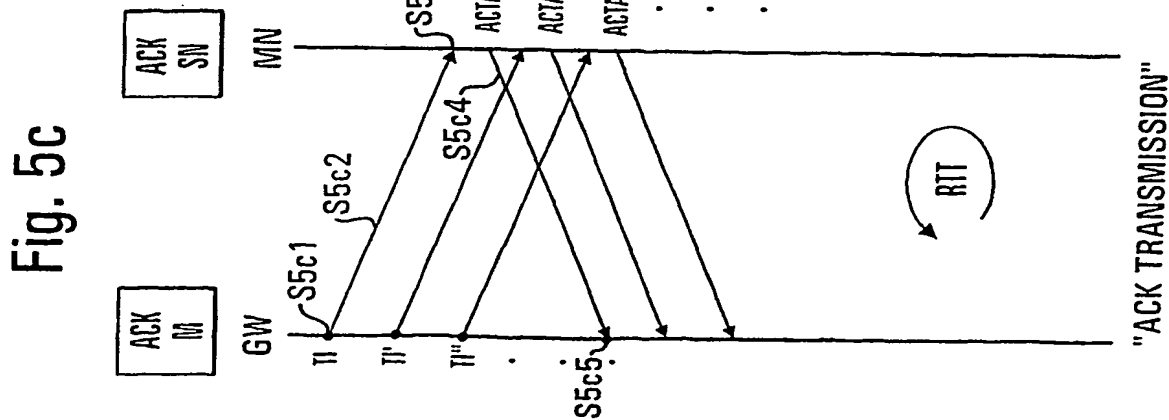
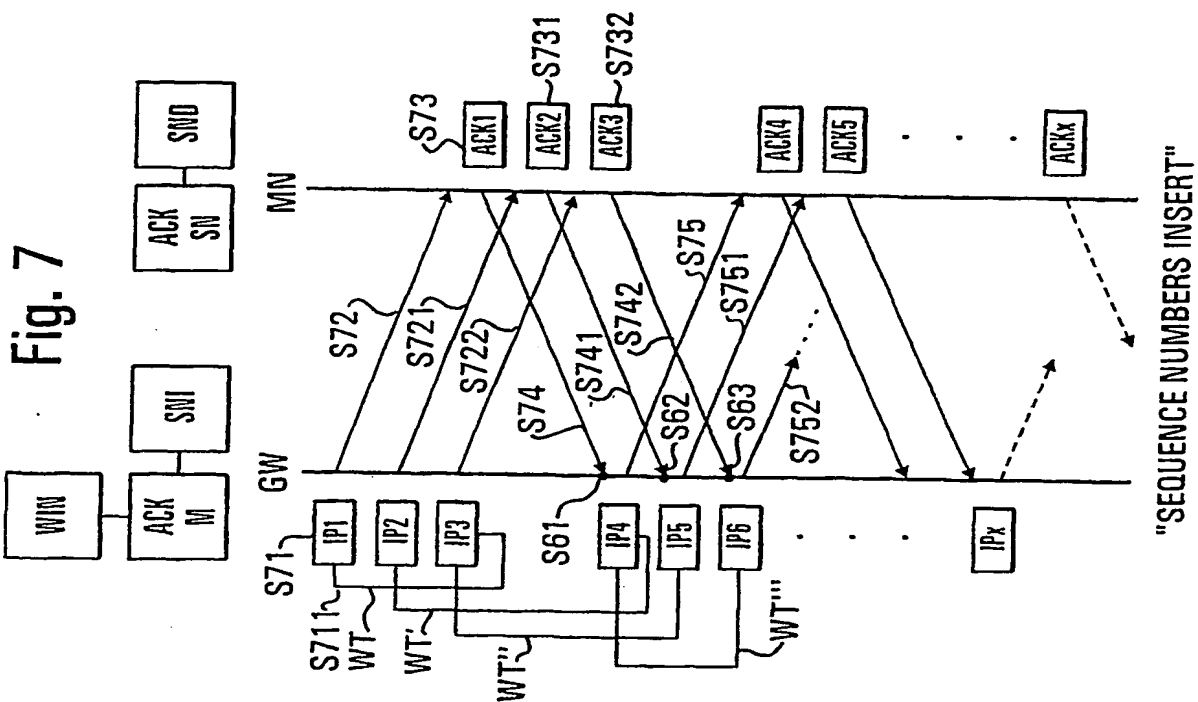
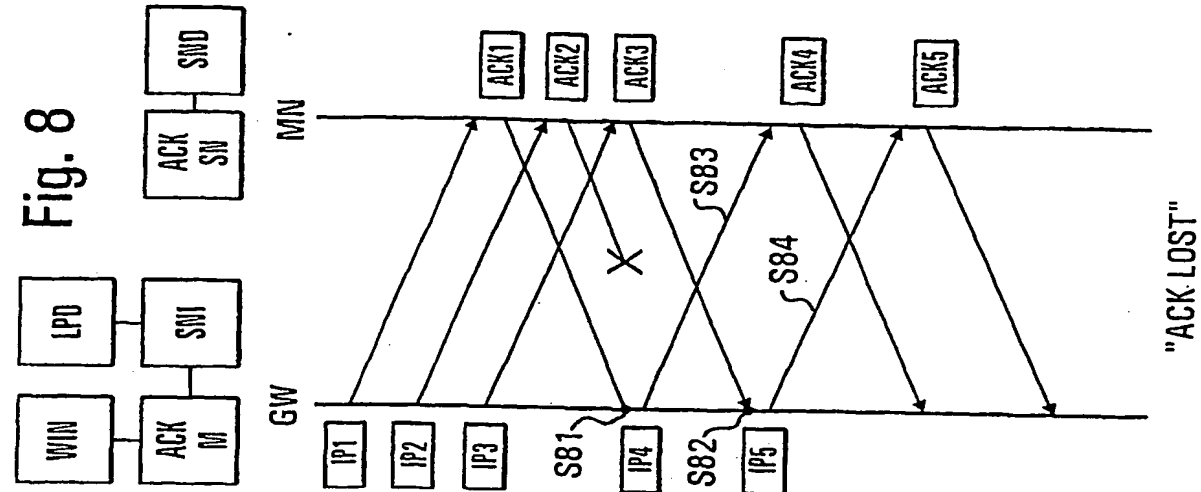


Fig. 5b

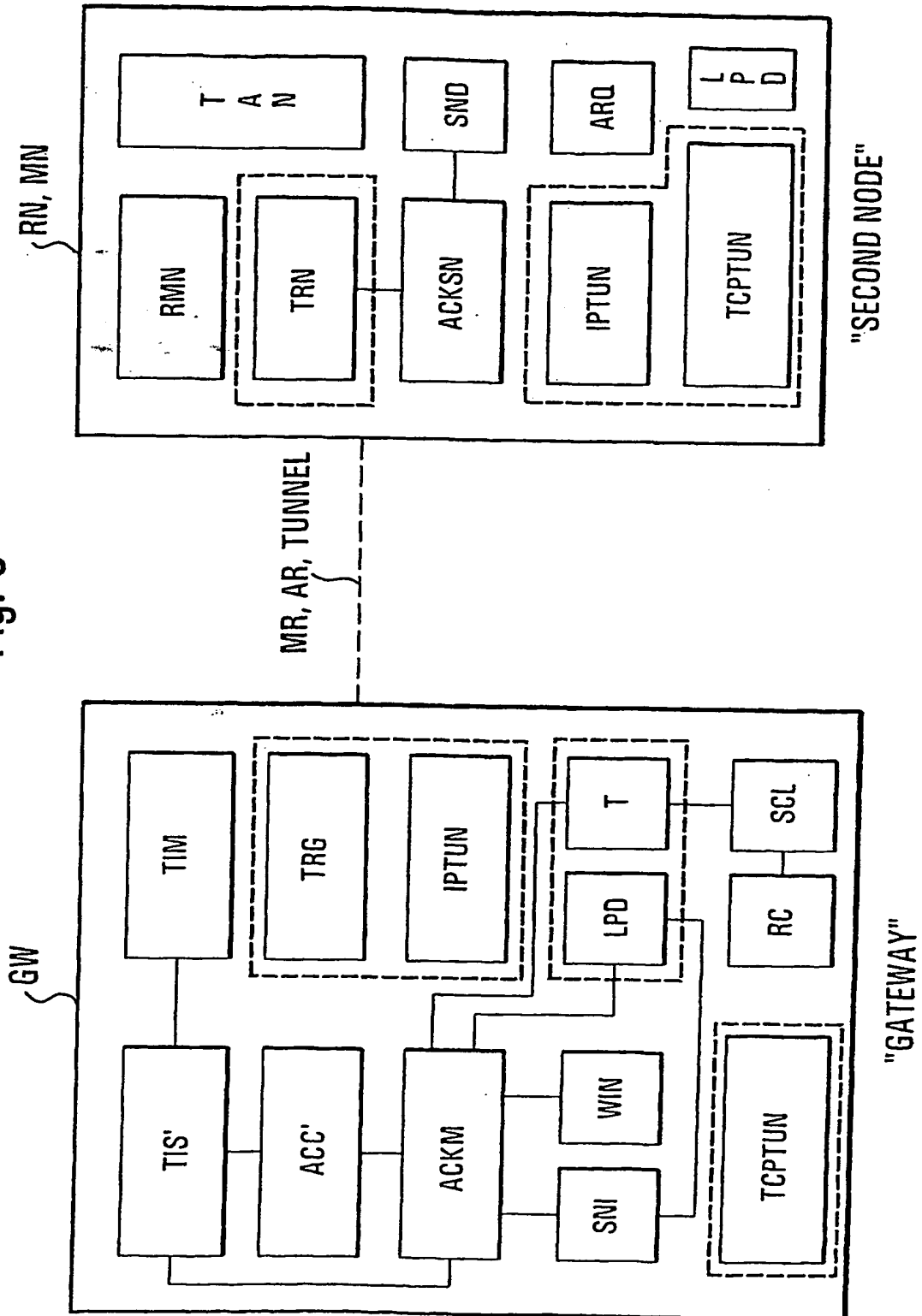


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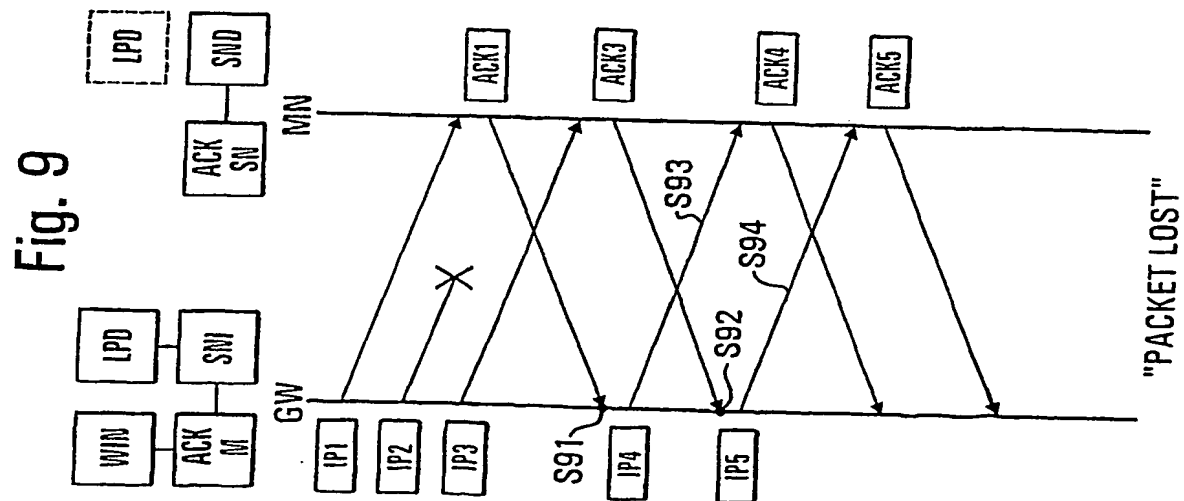
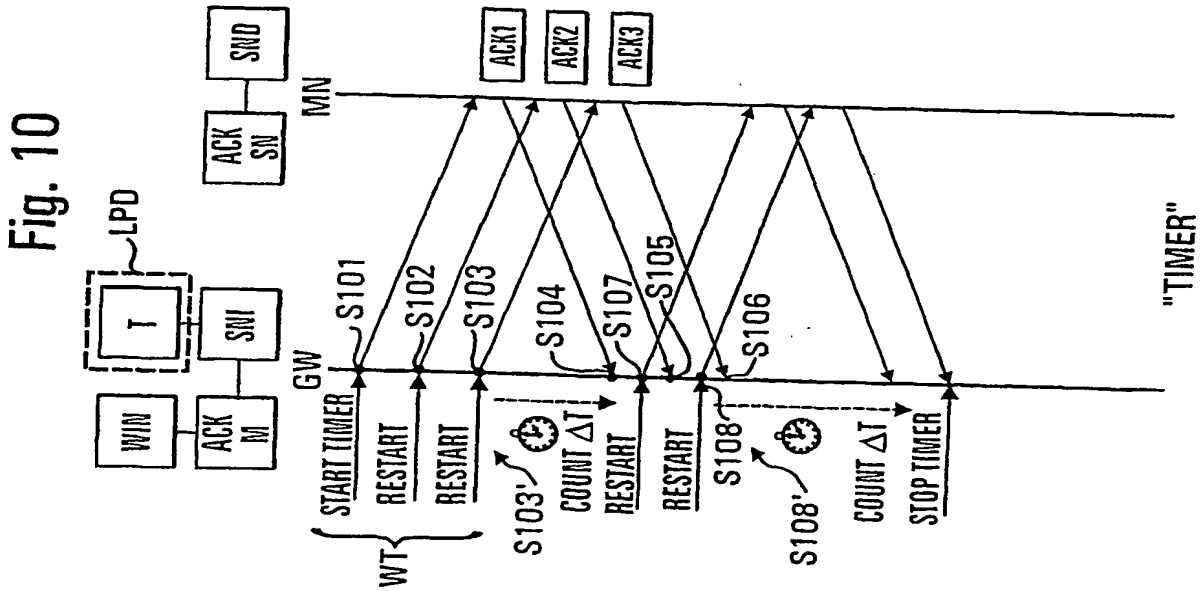
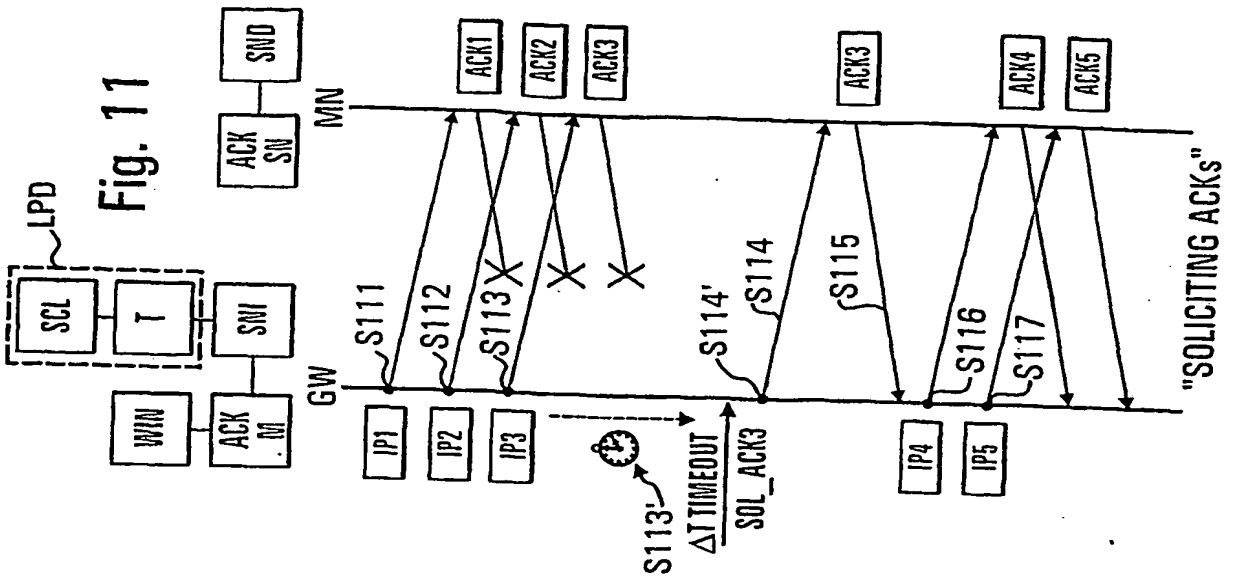


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Fig. 6



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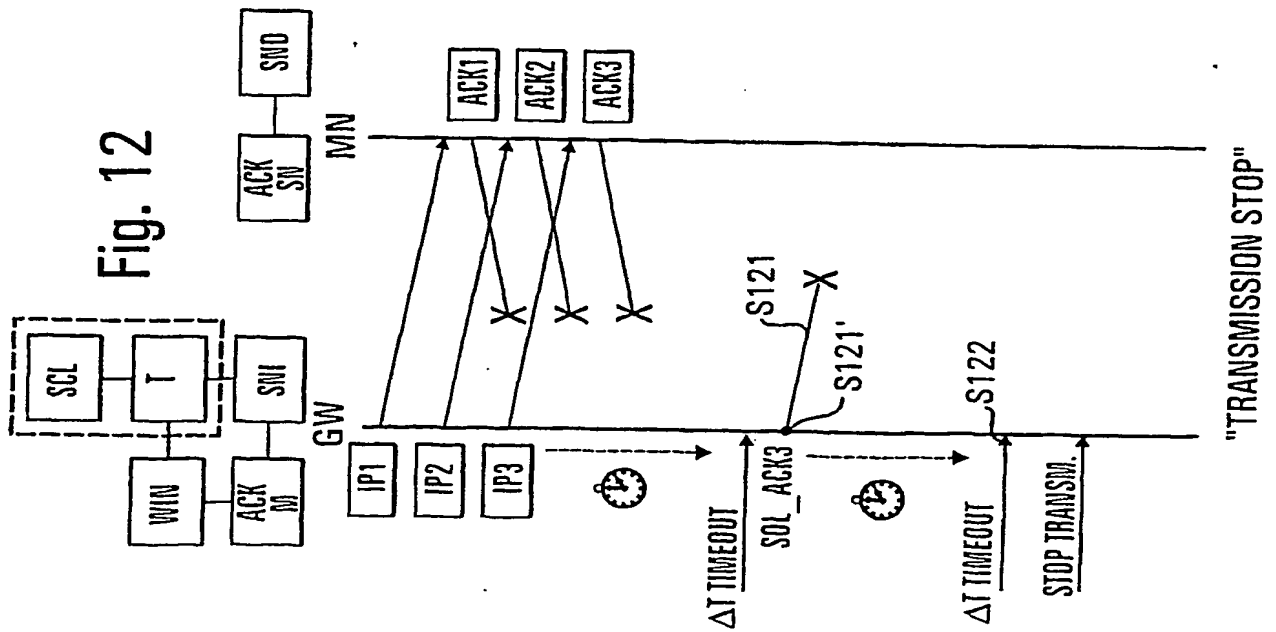
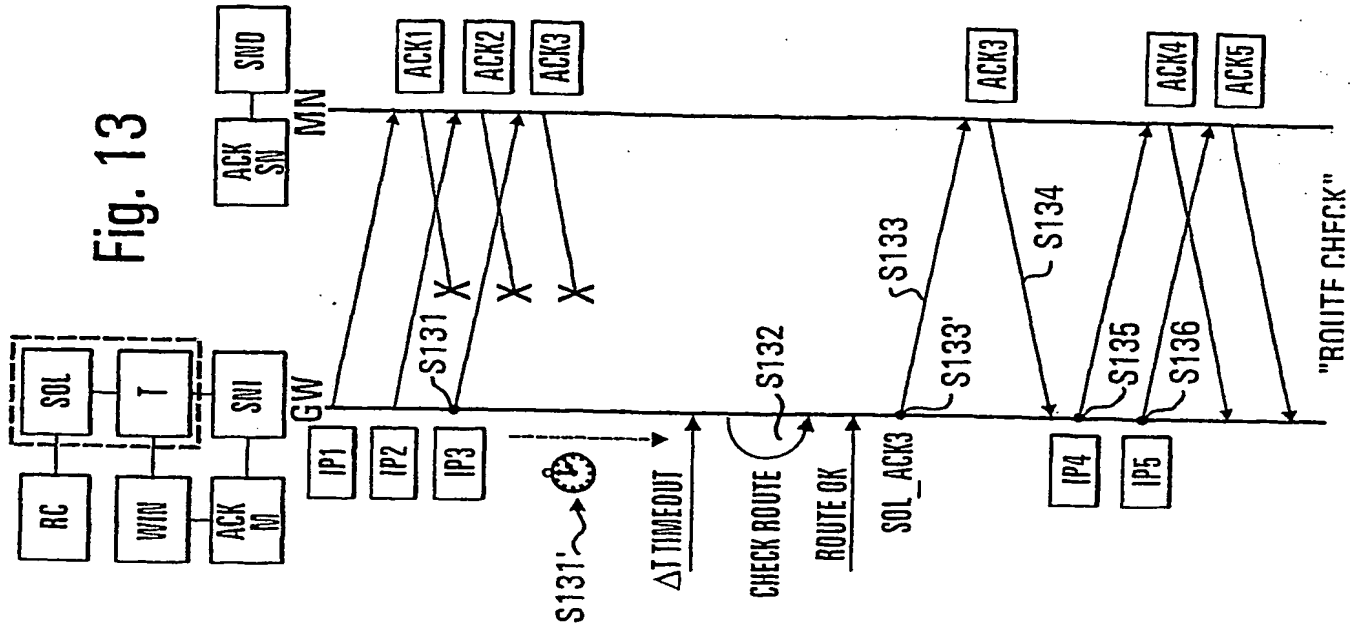


Fig. 15

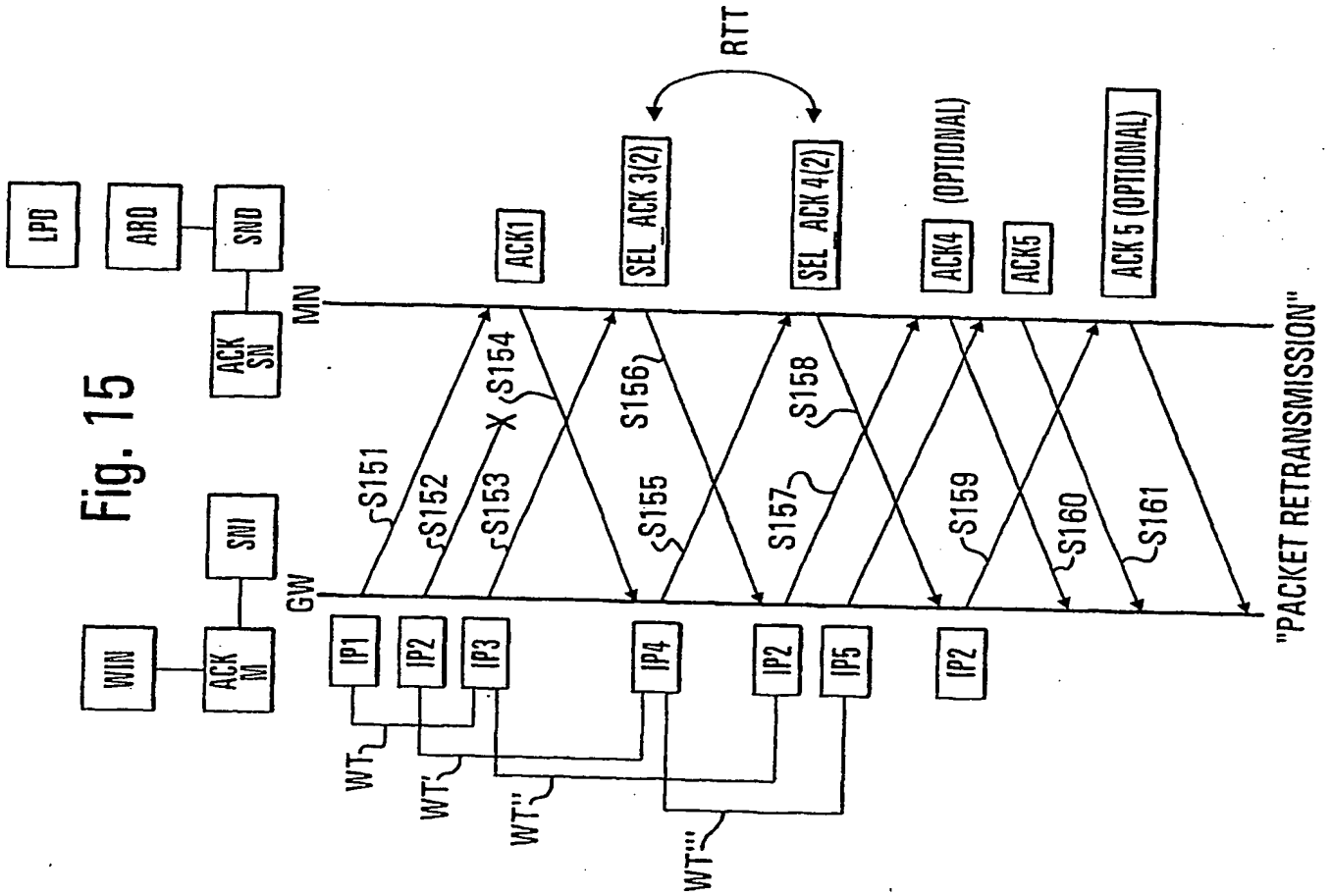


Fig. 14

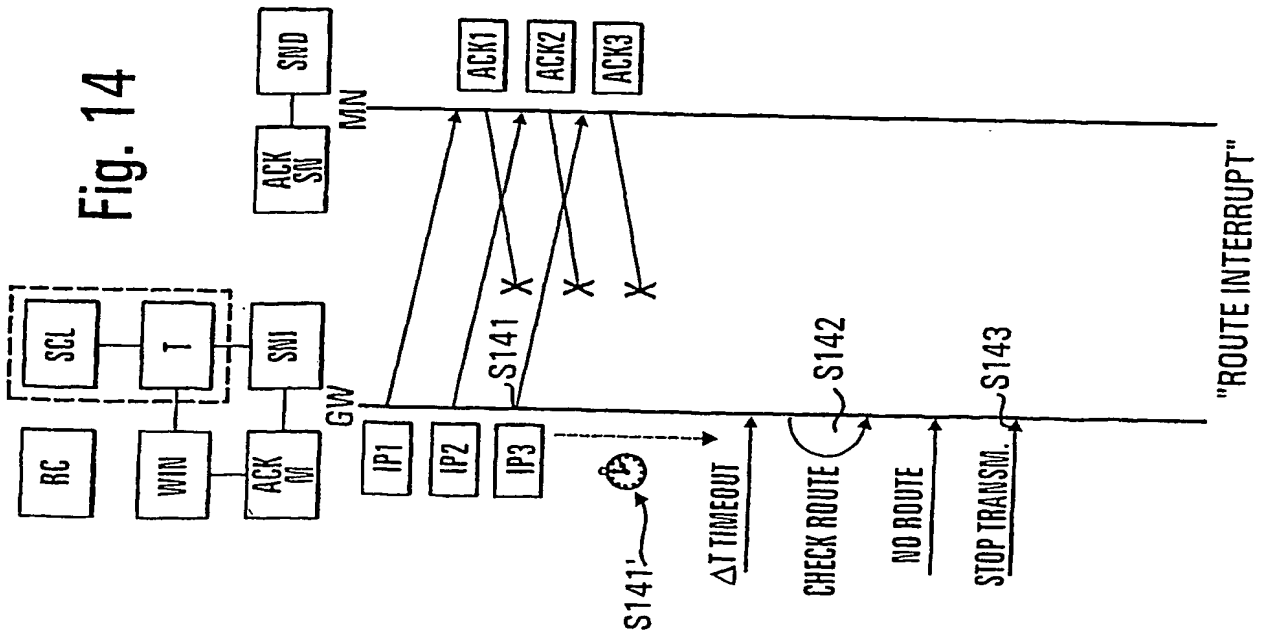


Fig. 16a

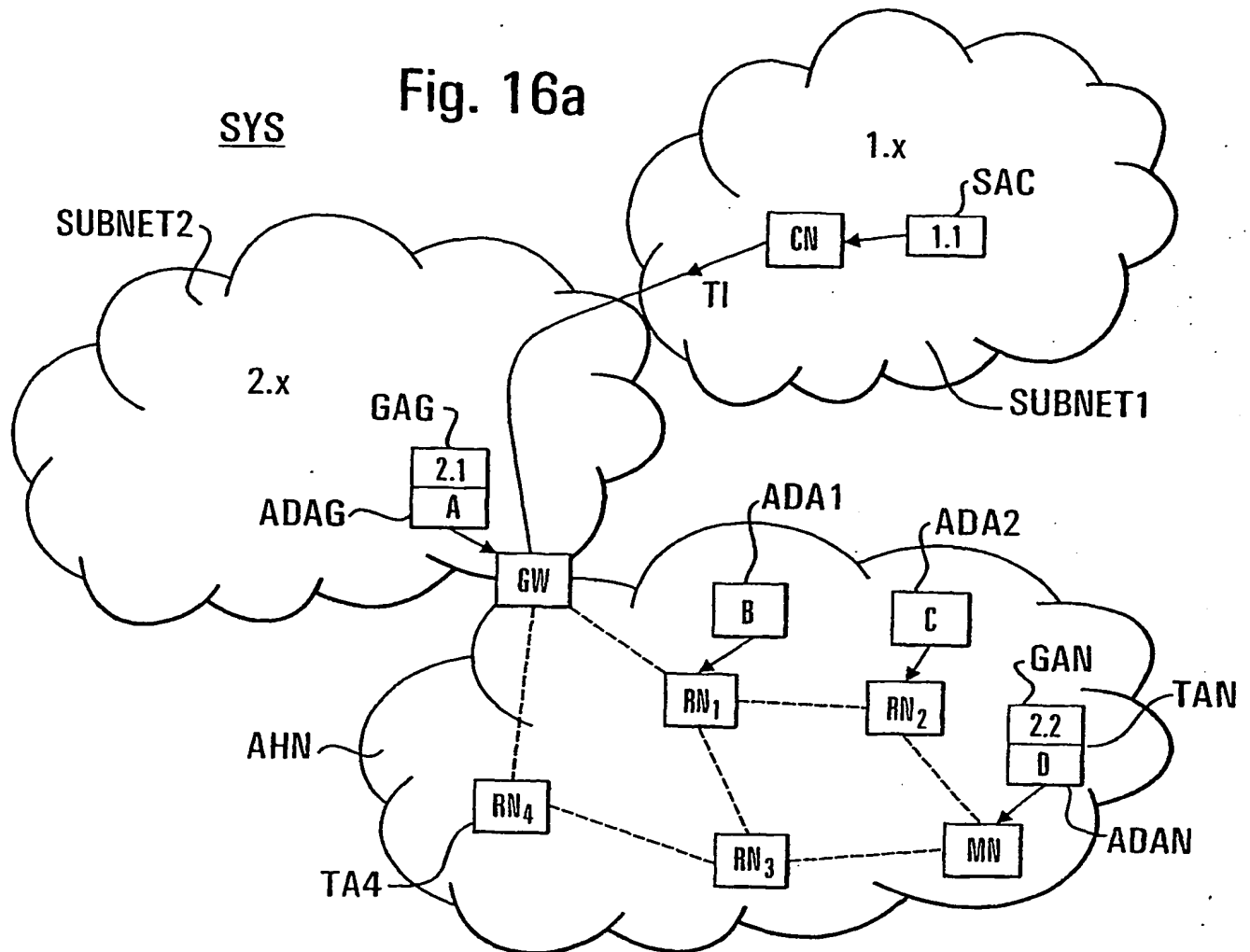
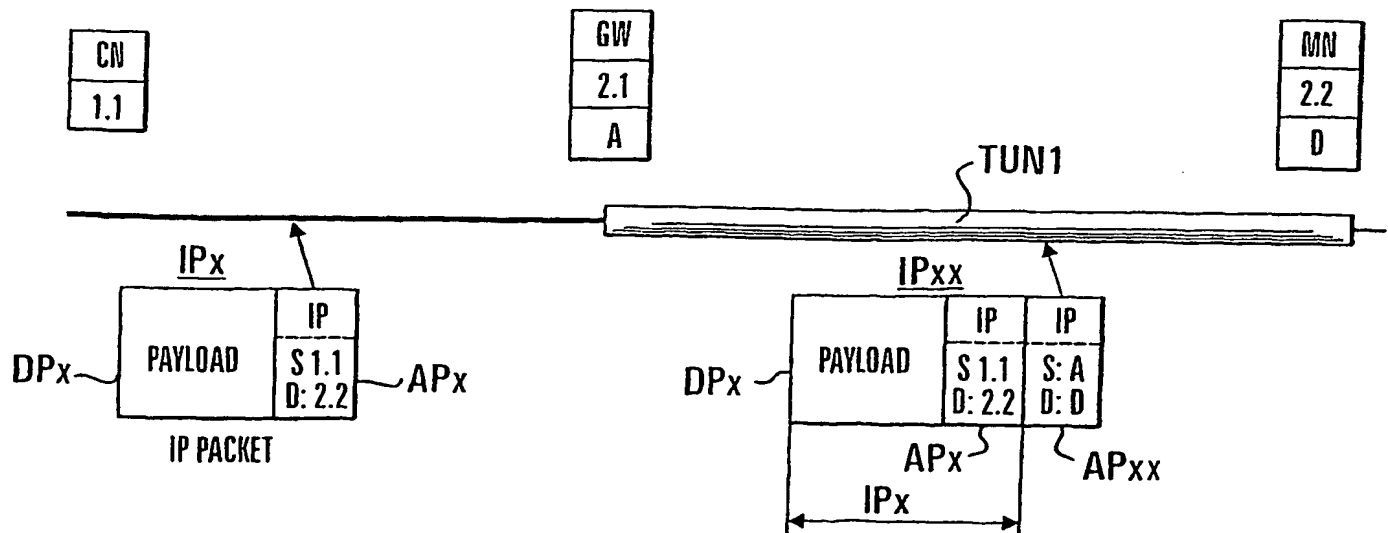


Fig. 16b



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Fig. 17

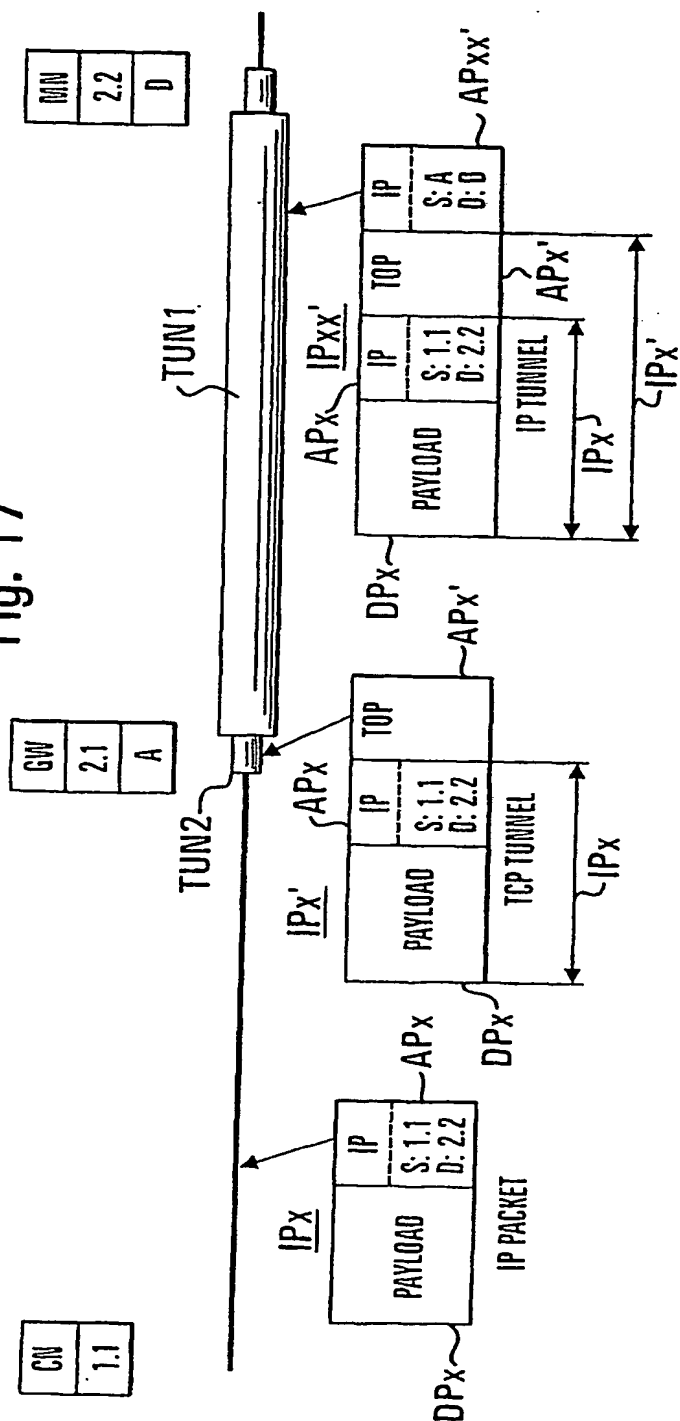


Fig. 18

